

Serial No.: 09/737,455
Attorney Docket No.: AUS9-2000-0596-US1

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1, 7 and 12 have been amended.

1. (currently amended) A method for supplying power to a device bus-controlled component of a computer system, comprising:
supplying power to the device bus-controlled component from an integrated power supply via a bus connection of the computer;
determining whether the power supplied exceeds a threshold; and
if the threshold is exceeded, supplying power to the device bus-controlled component from a non-integrated power supply of the computer.
2. (original) The method of claim 1, wherein if the threshold is exceeded the integrated power supply supplies power up to the threshold and the non-integrated power supply supplies any excess power.
3. (original) The method of claim 1, wherein if the threshold is exceeded the non-integrated power supply supplies all the power.
4. (currently amended) The method of claim 1, wherein high-power components on the device bus-controlled component are supplied power from the non-integrated power supply and low-power components on the device are supplies power from the integrated power supply.
5. (currently amended) The method of claim 1, wherein the device is a bus-controlled component and the integrated power supply is a bus slot capable of receiving the bus-controlled component.

6. (original) The method of claim 1, wherein a power sensor is used to determine whether the threshold has been exceeded.

7. (currently amended) A bus power system of a computer for supplying power to a bus-controlled component, comprising:
a bus slot supplying power to the bus-controlled component; and
a bus power handling device connected directly to a power supply of the computer for supplying power directly ~~from a power supply~~ to the bus-controlled component if a bus slot power threshold is exceeded.

8. (original) The bus power handling device of claim 7, wherein the bus power handling device is disposed between the bus slot and the bus-controlled component.

9. (original) The bus power system of claim 7, wherein the bus power handling device is disposed on the bus-controlled component.

10. (original) The bus power system of claim 8, further including a modified bracket attached to the bus-controlled component for securing the bus-controlled component within a computer case.

11. (original) The bus power system of claim 7, further comprising a power sensor disposed on the bus power handling device that determines whether the bus slot power threshold has been exceeded.

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12. (currently amended) A bus power handling device of a computer for supplying power to a bus-controlled component inserted into a bus slot supplying power to the component, comprising:

an input area configured to receive a bus-controlled component and an output area configured to be inserted into a bus slot that supplies power to the bus-controlled component;

a power sensor that determines whether the bus slot has exceeded a power threshold; and

a power supply lead that ~~supplies power from a~~ coupled to an external power supply that supplies power to the bus-controlled component via the bus power handling device if the power threshold is exceeded.

13. (original) The bus power handling device of claim 12, wherein the bus-controlled component obtains power from the bus slot until the power threshold is exceeded at which time the power is obtained from the power supply.

14. (original) The bus power handling device of claim 12, wherein the bus-controlled component obtains power from the bus slot and any power in excess of the power threshold from the power supply.

15. (original) The bus power handling device of claim 12, wherein the power threshold is a maximum power allowed by a computer bus standard for the bus slot.

16. (original) The bus power handling device of claim 12, wherein bus slot supplies power to low-power devices on the bus-controlled component and the power supply supplies power to the high-power devices on the bus-controlled component and wherein the power supplied by the bus slot does not exceed the power threshold.